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will add up.

NEW QUESTION: 1

Your development team uses Microsoft Visual Studio Team Foundation Server (TFS) 2012 with the Microsoft Framework (MSF) for Capability Maturity Model Integration (CMMI) template. You are reviewing outstanding issues with your team and trying to agree on whether you can mark a particular issue as resolved.

You need to determine whether the issue has been resolved. Which two criteria must the team meet before the issue can be marked as resolved? (Each correct answer presents part of the solution. Choose two.)

- A. All impediments under an issue have been completed.
- B. All tasks under an issue have been completed.
- C. All user stories under an issue have been completed.
- D. Stakeholders are satisfied that the issue has been resolved.

Answer: B,D

NEW QUESTION: 2

Note: This question is part of a series of questions that present the same scenario. Each question in the series contains a unique solution that might meet the stated goals. Some question sets might have more than one correct solution, while others might not have a correct solution.

After you answer a question in this section, you will NOT be able to return to it. As a result, these questions will not appear in the review screen.

A customer needs to start a new agreement by making only an annual Monetary Commitment of \$25,000 US dollars for Microsoft Azure services.

Solution: The customer uses a Microsoft Products and Services Agreement (MPSA) to purchase the services.

Does this meet the goal?

- A. No
- B. Yes

Answer: A

NEW QUESTION: 3

You have been asked to add 2 new PCI-X cards into an existing server architecture and notice that they run at different clock speeds. The system has both shared and dedicated PCI bus slots available. In order to maximize performance, which PCI cards should be installed in which slots? (Choose two.)

- A. PCI-X 100 installed in a shared bus slot
- B. PCI-X 100 installed in a dedicated bus slot
- C. PCI-X 133 installed in a dedicated bus slot
- D. PCI-X 133 installed in a shared bus slot
- E. PCI-X bus slots are speed independent

Answer: A,C

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